



# ADVANCED ELECTRONICS LABORATORIES



## WORKSHOP ON CHIP PROCESS DESIGN & ANALYTICAL TESTING

Advanced Electronics Laboratories offer a 5-days workshop on **CHIP PROCESS DESIGN AND ANALYTICAL TESTING** for all those graduate students who are entering into research phase from summer 2015. The lab engineers and faculty members from Faculty of Engineering & Technology (Electronic Engineering and Mechanical Engineering) are also encouraged to attend the workshop to gain the knowledge of physical processes of IC design as well as post-process reliability, efficiency and failure analysis of devices and microsystems. The training will be conducted on internationally licensed softwares and R&D grade sophisticated machines in the laboratories.

**Day 1:** Chip Design:  
Physical Processes

**Day 2:** Electrical  
Characterization

**Day 3:** Advanced  
Electrical  
Characterization

**Day 4:** Electro-optical  
Assessment & Rapid  
Thermal Processing

**Day 5:** Back End  
Processes

**Days:** 29<sup>TH</sup> JUNE- 3<sup>RD</sup> JULY, 2015

**Time:** (9:30 AM – 2:00 PM)

**Venue:** Advanced Electronics  
Laboratories, Ibn-Khaldun Block

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