

Muhammad Shuaib		
<i>Assistant Professor</i>		
muhammad.shuaib@iiu.edu.pk		
Academic Qualification		
MS	Electrical Engineering (System-On-Chip)	
BS	Electrical Engineering (Electronics and Communication)	
Professional Experience		
Assistant Professor	IIUI	(01-01-2010 to date):
Assistant Manager (Electrical Engineer)	GEPCO, WAPDA	2003- To- 2006
Assistant Director	Punjab Industries, Mines and Mineral Department (Lahore)	(2002-2003)
Research Supervision		
BS/BSc FYDPs	10	
MS Research		
PhD Research		
Funded Projects		
Publications		
Conference	4	
Journal	1	
Books/Chapters	1	
Publications' Link		
Journal/Conference Editor		
Invited Talks (Conference)		
Awards/Achievements		
International/National Collaborations		
Skills		
Misc.	Cadence (HDL, Schematic, Virtuoso, LVS, Assura, Calibre, Spectre /Spectre RF), Mentor graphic IC Design. ADS. LT Spice, Hspice, Electric VLSI. High-Level circuit modelling languages such as VHDL, Verilog, and Verilog-A, MATLAB& Simulink	