| Ayesha Abbasi | | |
|--------------------------------|--|--|
| | Lecturer | |
| avesh | a.abbasi@iiu.edu.pk | |
| Academic Qualification | | |
| PhD | PhD Electronics Engineering (Specialization Power and Controls) | International Islamic University, Islamabad |
| MS | MS Electrical Engineering (Digital Signal Processing) | National University of Sciences and Technology (NUST), Islamabad |
| BS | BE Electrical Engineering (Majors in Electronics) | Air University |
| Professional Experience | · · · · · · · · · · · · · · · · · · · | · |
| Lecturer | International Islamic University | 2018 - To Date, Lecturer, |
| Lecturer | Air University | 2013 - 2018 |
| Lab Engineer | Air University | 2008 - 2013 |
| Research Supervision | | |
| BS/BSc FYDPs | 10 | |
| MS Research | | |
| PhD Research | | |
| Funded Projects | | |
| Publications | | |
| Conference | 2 | |
| Journal | 3 | |
| Books/Chapters | | |
| Publications' Link | https://scholar.google.com/citations?u | ser=vPcrAiAAAAAJ&hl=en |
| Journal/Conference Editor | | |
| Invited Talks | | |
| (Conference) | | |
| Awards/Achievements | | |
| Mentor | Invited Mentor, Women's leadership program, USAID Higher Education System Strengthening Program (HESSA) 08/2022- 02/2023 | |
| Position | First Position in Master Trainer under HEC 28th Masters Trainers Program 07/2016-09/2016 | |
| International/National (| | |
| NIL | | |

| Skills | | |
|--------------------|---|--|
| Tools | MATLAB, Simulink, LABVIEW, OrCAD - PCB Design Software and Schematic Editor, Proteus, LaTeX/LyX, AutoCAD, XILINX, Pspice, Circuit Maker, Electronic WorkBench, PSpice | |
| Languages | C/C++ with OOP, Verilog HDL, Assembly, Ladder Logic | |
| Operating Systems: | Linux, DOS, Windows 98/2000/ XP/7/8/10 | |