

RESUME

FARYAL SIDDIQUE

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OBJECTIVE:

To grow and develop in a competitive environment that positively challenges my Inter-personal and intellectual ambitions while better equipping, and instilling, me as an integral part of society.

PERSONAL INFORMATION:

Date of Birth: 17-01-1991
Nationality: Pakistani
PEC Reg. No. Electro/19094

ACADEMIC QUALIFICATION:

Degree	Institute	Session	CGPA/ %age
PhD Electrical Engineering	IIU, Islamabad	2018-2021	In progress
MS Electronic Engineering	IIU, Islamabad	2014-2016	3.60/4.00
BS Electronic Engineering	IIU, Islamabad	2009-2013	3.73/4.00
H.S.S.C (pre-Engineering)	Leadership College, Multan.	2005-2007	81.64 %
S.S.C (science group)	R.P. High School, Shujabad.	2003-2005	85.05 %

COMPUTER PROFICIENCY AREA:

Agilent ADS (Advance Design System)
Mentor Graphics (ADK)
SILVACO (TCAD)
PRO- Engineering
LT spice
NI Multisim
SCAPS
SRIM

EXPERIENCE:

Internee – NESCOM (March 2013- June 2013)
Lab. Engineer Visiting – IIUI (October 2013 – January 2014)
Lab Engineer – IIUI (July 2014 – Till Date)

PROFESSIONAL TRAINING

Silvaco TCAD by IC mic Malaysia
Multiple OBE Trainings at DEE-FET-IIUI

